

Code: 9F00203

MCA - II Semester Regular & Supplementary Examinations, August/September 2012

COMPUTER ORGANIZATION

(For students admitted in 2009, 2010 & 2011 only)

Time: 3 hours

Max Marks: 60

Answer any FIVE questions
All questions carry equal marks

- 1 Explain combinational and sequential circuits in detail.
- 2 Discuss 2-way set associative mapping and 4-way set associative mapping techniques with an example.
- 3 Explain design of control unit and micro program sequencer for a control memory.
- 4 Write a short note on the following:
 - (a) Instruction formats.
 - (b) Addressing modes.
- 5 Explain architecture of 8086 with a neat sketch.
- 6 Discuss about DMA in detail.
- 7
 - (a) Explain RISC pipeline in detail.
 - (b) Write a short note on vector processing.
- 8
 - (a) Explain the following terms:
 - (i) Read Miss (ii) Read Hit (iii) Write Miss (iv) Write Hit
 - (b) Explain the concept of inter process arbitration.
